

Device function configuration table

series		MH2103A					
model		CBT6	CCT6	RPT6	VET6	VGT6	ZET6
Flash memory		128	256	256	512	1024	512
(K bytes)							
SRAM		32	64	96	96	96	96
(k bytes)							
timer	senior	1	1	2	2	2	2
	currency	4	4	10	10	10	10
	basic	2	2	2	2	2	2
Communication interface	SPI	3	3	3	3	3	3
	12S	-	-	2	2	2	2
	12c	2	2	2	2	2	2
	USART/UART	3	3	5	5	5	5
	USB	1	1	1	1	1	1
	CAN	1	1	1	1	1	1
	SDIO	-	-	1	1	1	1
FSMC		-	-	-	1	1	1
GPIO port		37	37	51	80	80	112
12-bit ADC module (number of channels)		2 (10 channels)	2 (10 channels)	3 (16 channels)	3 (16 channels)	3 (16 channels)	3 (21 channels)
12-bit DAC module (number of channels)		2 (2 channels)	2 (2 channels)	2 (2 channels)	2 (2 channels)	2 (2 channels)	2 (2 channels)
Random number module		support					
Hardware encryption algorithm unit		support					
Page size (K bytes)		1	2	2	2	4	2
CPU frequency		216M					
Operating voltage		2.0`3.6v					
Operating temperature		-40 TO +85°C					
Encapsulation form		LQFP48		LQFP64	LQFP100		LQFP144